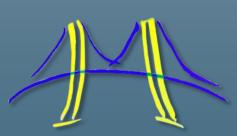
A SPECIALIZATION FRAMEWORK FOR AUDIO CONTENT ANALYSIS

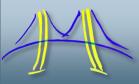
Katya Gonina with Henry Cook, Eric Battenberg, Gerald Friedland* and Kurt Keutzer

UC Berkeley ParLab, *International Computer Science Institute

January 18, 2012





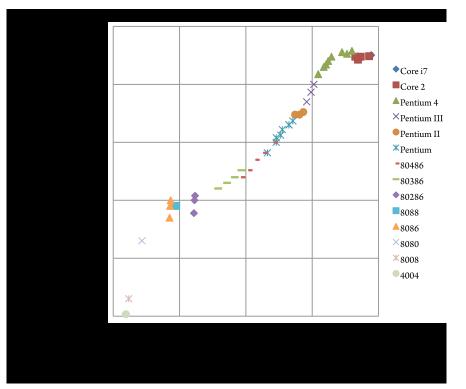


The shift to parallel processing

Parallel processing is here

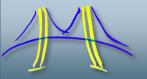
** This shift toward increasing parallelism is not a triumphant stride forward based on breakthroughs in novel software and architectures for parallelism; instead, this plunge into parallelism is actually a retreat from even greater challenges that thwart efficient silicon implementation of traditional uniprocessor architectures.

- The Berkeley View [1]



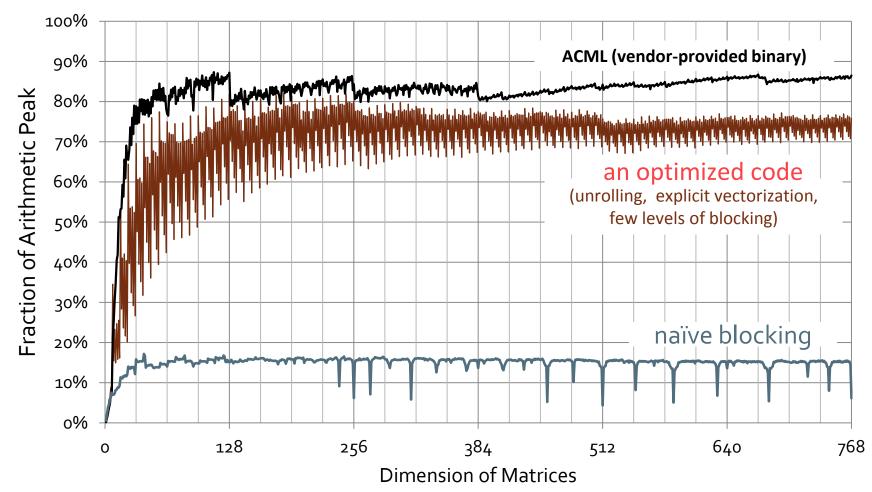
[1] Krste Asanovic et al. "The Landscape of Parallel Computing Research: A View from Berkeley" December 2006

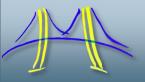
Intel Processor Clock Speed



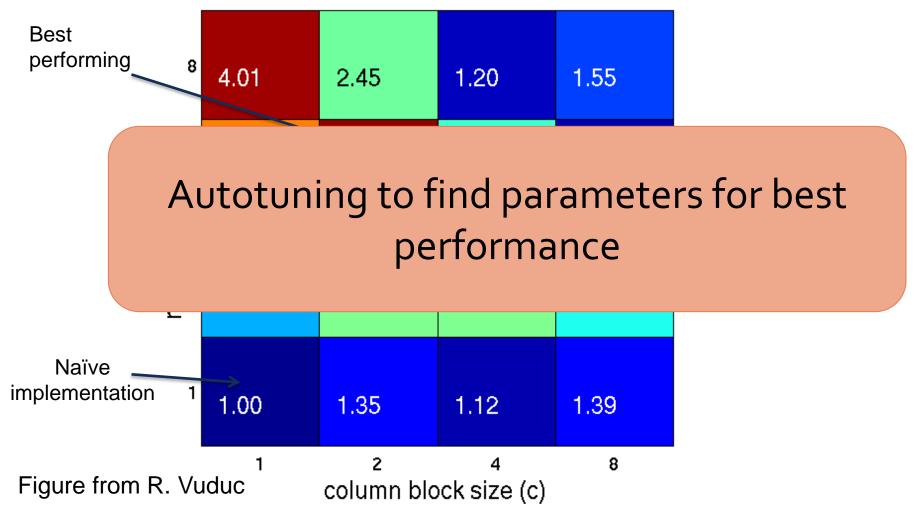
Writing Fast Code is Hard

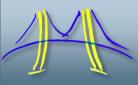




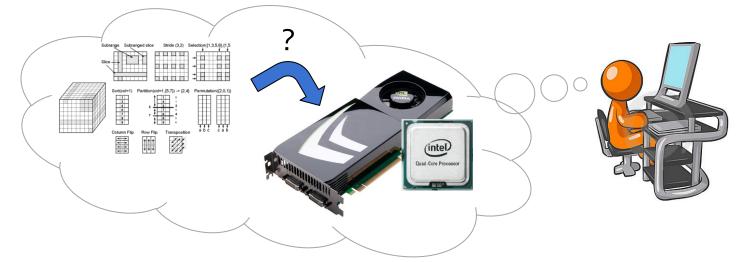


900 MHz Itanium 2, Intel C v8: ref=275 Mflop/s

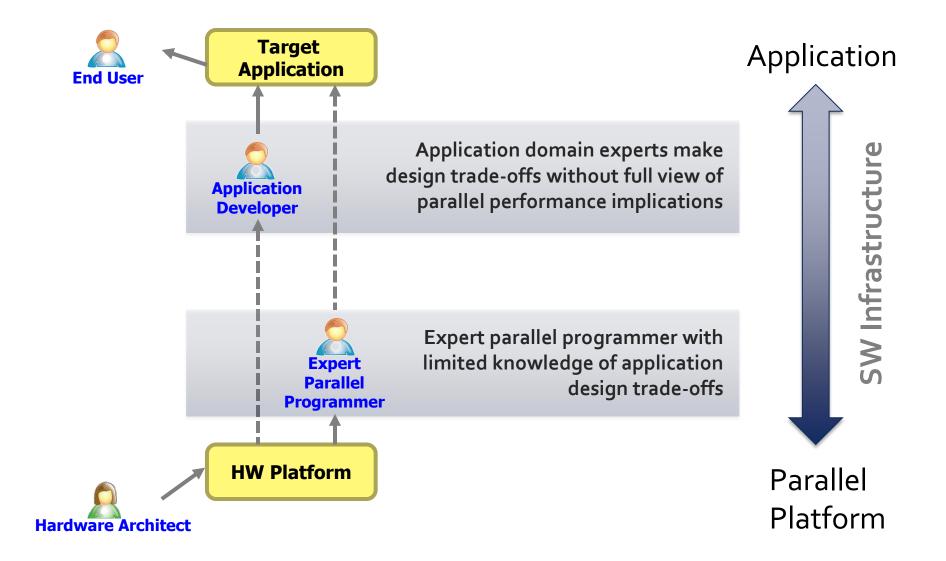




- Domain experts prefer to use high-level languages such as Python or MATLAB
- However, to achieve sufficient performance, computationally-intensive parts of applications must be rewritten in low-level languages
- Parallel platform and input parameters determine the best-performing parallel implementation

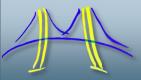








- 1. Parallelism & productivity-performance gap 🗸
- 2. Proposed solution: Just-in-time specialization
- 3. Example: Gaussian mixture model (GMM) training specializer
- 4. Example applications using GMM specializer:
 - 1. Speaker diarization
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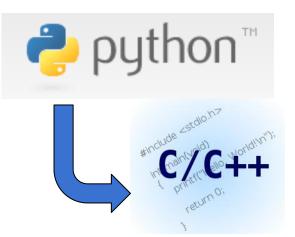
Key Idea: Generate, compile, and execute high performance parallel code at runtime using code transformation, introspection, variant selection and other features of highlevel languages [2].

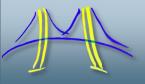
Invisibly to the user.

[2] B. Catanzaro, S. Kamil, Y. Lee, K. Asanovic, J. Demmel, K. Keutzer, J. Shalf, K. Yelick, and A. Fox. SEJITS: Getting productivity and performance with selective embedded JIT specialization. In Workshop on Programming Models for Emerging Architectures (PMEA 2009), Raleigh, NC, October 2009.

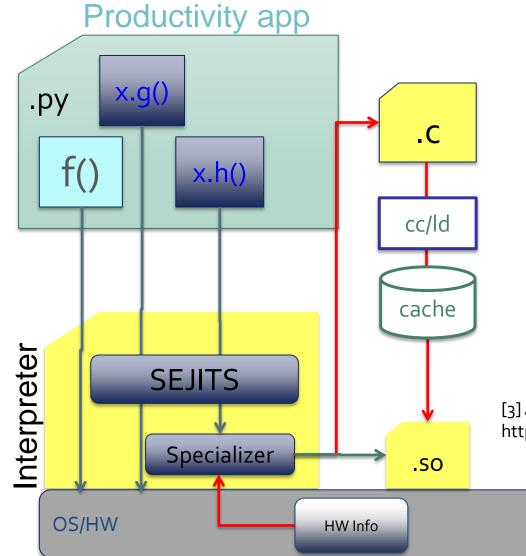


- Productivity-level language (PLL), e.g. Python for applications
- "Specializers" generate efficiency-level language (ELL) code targeted to hardware
 - Specialize specific computation
 - Code generation happens at runtime
 - Specializers can incorporate autotuning
- ELL performance with PLL effort



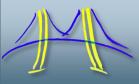


Selective Embedded JIT Specialization (SEJITS)



Asp – A SEJITS for Python [3]

[3] Asp: A SEJITS implementation for Python. https://github.com/shoaibkamil/asp



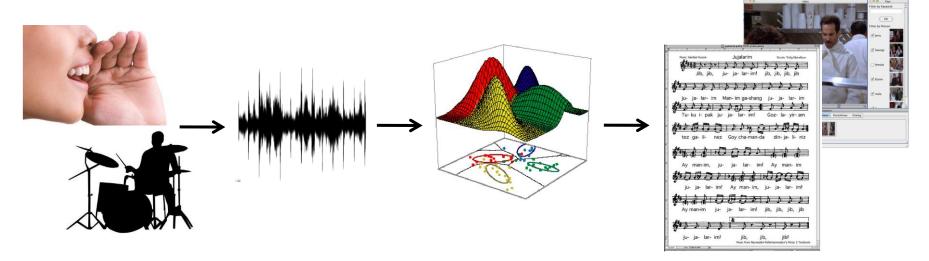
- For productivity programmers
 - Efficient performance from high-level language



- Further improvements in performance as specializers are added/refined
- More programmers can exploit parallel architectures
- Application code far more *portable* & *maintainable*
- For parallel programming experts
 - Provide useful common infrastructure for creating fast specializers
 - Wider impact & code reuse



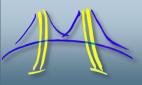
 Pattern recognition and information extraction from audio files



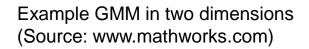
- Have impact on a big market
- Are computationally demanding
- Require processing large sets of data
- Have specific throughput and real-time constraints

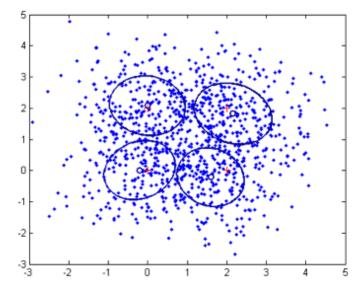


- 1. Parallelism & productivity-performance gap
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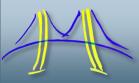


- Probabilistic model for clustering data
 - Assumes the distribution of observations follows a set (mixture) of multidimensional Gaussian distributions
 - Each Gaussian in the mixture has a mean (*II*) and a covariance (*S*) parameters
 - Gaussians in the mixture are weighted with weight *D*





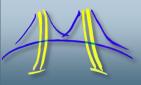
$$p(x_j \mid \mu_i, \Sigma_i) = \sum_i \pi_i rac{1}{(2\pi)^{rac{D}{2}} \mid \Sigma_i \mid^{rac{1}{2}}} exp\{-rac{1}{2}(x_j - \mu_i)^T \Sigma_i^{-1}(x_j - \mu_i)\}$$



- Given a set of observations/events find the maximum likelihood estimates of the set of Gaussian Mixture parameters (*π*, Σ, *ρ*) and classify observations
- Expectation Maximization (EM) Algorithm
 - E step
 - Compute probabilities of events given model parameters
 - M step
 - Compute model parameters given probabilities
 - weights, mean, covariance matrix
 - Iterate until convergence

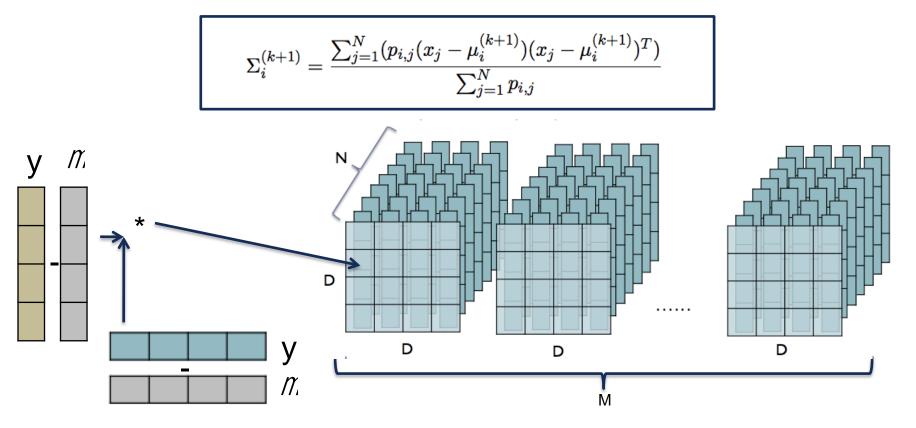
Covariance matrix – most computationally intensive step

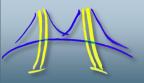
Based on original GPU implementation by [4]. A. D. Pangborn. Scalable data clustering using gpus. Master's thesis, Rochester Institute of Technology, 2010.



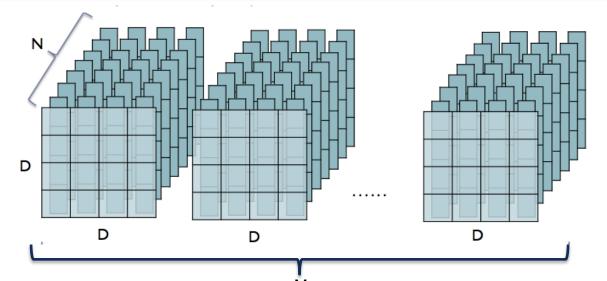
Covariance Matrix Computation

- N number of feature vectors, ~10K-1M
- D feature vector dimension, ~10-100
- M number of Gaussian components, ~1-128
- Matrix is symmetric only compute the lower D*D/2 cells

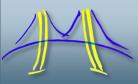




Covariance Matrix Computation



- Opportunities for parallelism (independent computations):
 - Each component's covariance matrix
 - Each cell in a covariance matrix
 - Each feature vector's contribution to a cell in a covariance matrix
- -> Multiple code variants to perform the same computation in different ways (here: on Nvidia GPUs)

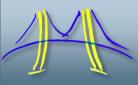


- CUDA is a recent programming model, designed for
 - Manycore (GPU) architectures
 - Wide vector (SIMD*) parallelism
 - Scalability
- CUDA provides:



- A thread abstraction to deal with SIMD
- Synchronization & data sharing between small groups of threads
- CUDA programs are written in C + extensions

*SIMD = "Single Instruction, Multiple Data"

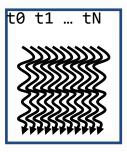


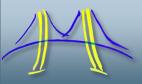
Threads and Thread blocks

- Parallel kernels composed of many threads
 - all threads execute the same sequential program
 - Kernels:
 - Invoked from "Host" CPU code (C)
 - Executed on the "Device" GPU
- Threads are grouped into thread blocks
 - threads in the same block can cooperate
- Threads/blocks have unique IDs

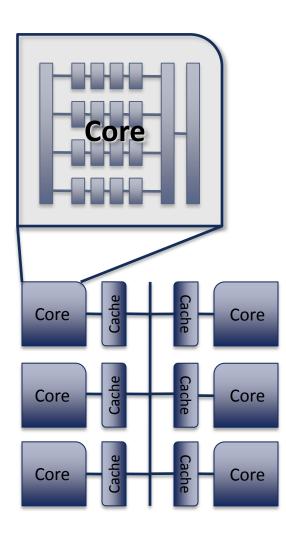




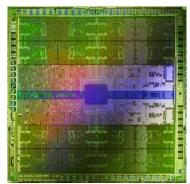




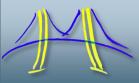
Manycore Parallel Platform



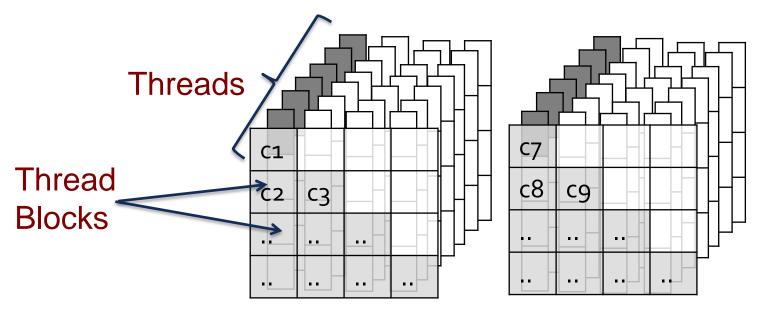
- Two levels of parallelism:
 - Cores
 - CUDA thread block
 - SIMD vector lanes within the core
 - CUDA threads
- Per-core local memory
 - Software Programmable
 - Shared by all threads in a thread block

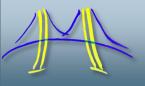


Nvidia GTX480 (Fermi) Die Photo

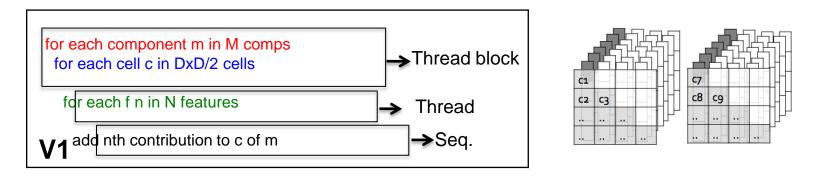


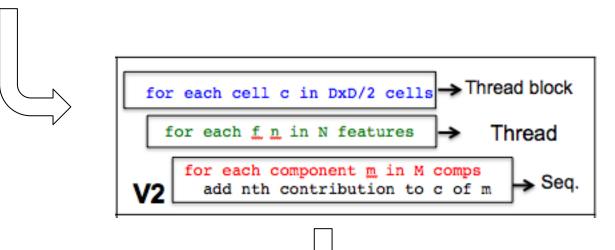
- Code variant 1:
 - 2D grid of thread blocks M x D*D/2
 - Each thread block is responsible for computing one cell in the covariance matrix for one component
 - Thread parallelization over feature vectors (N)

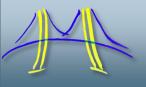




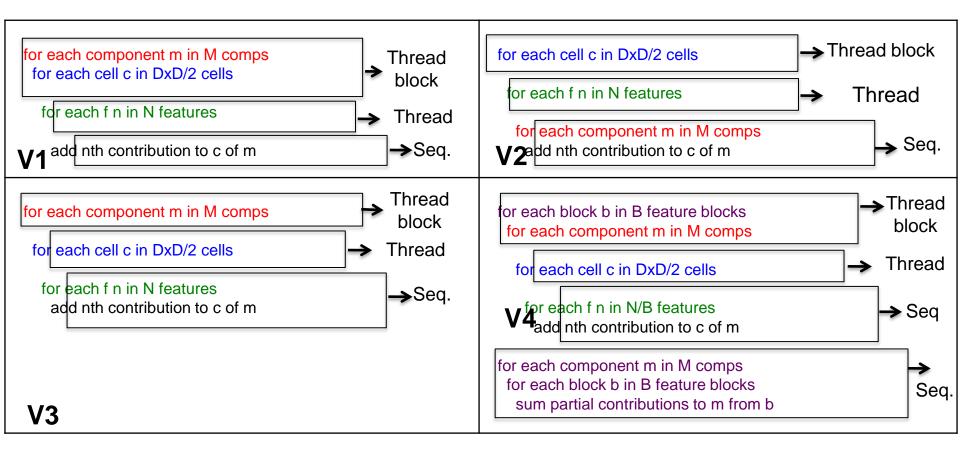
Covariance Matrix Computation – Code Variants







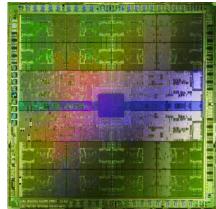
Covariance Matrix Computation – Code Variants Summary

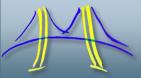


Specialization

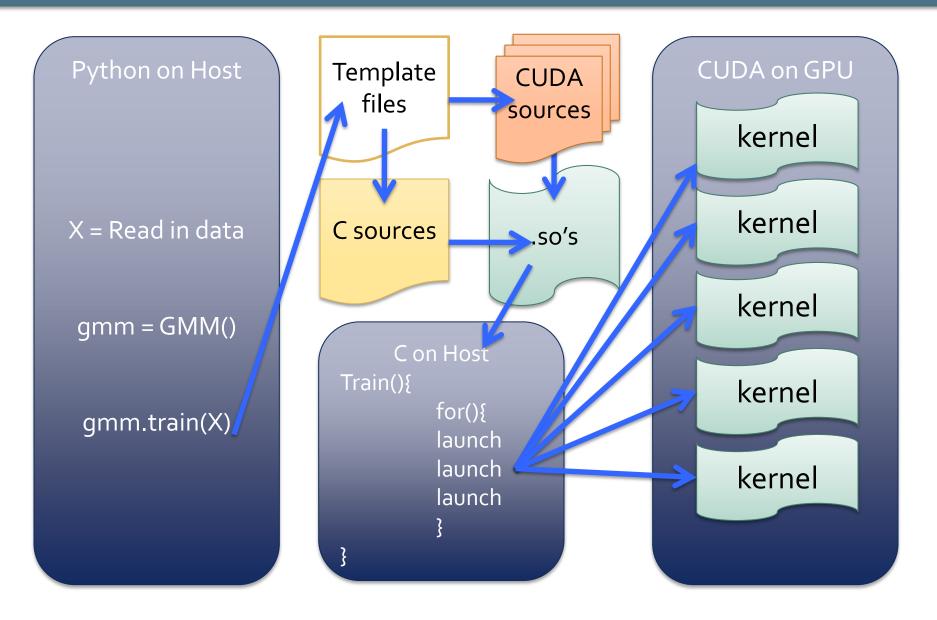
- Given:
 - Problem Dimensions (N, D, M)
 - Platform Parameters (targeting Nvidia GPUs)
 - Core count, local memory size, SIMD width...
- Automatically select:
 - Optimal code variant
 - Optimal parameters (block size, number of blocks) for that code variant

for each component m in N compo for each cell c in DxD/2 cells for each cell c in DxD/2 cells for each event n in N events Work item M ad nth contribution to c of m Seq.	for each cell c in DxD/2 cells Work group for each event n in N events Work item V2 for each component m in N comps add nth contribution to c of m Seq.
for each component <u>m</u> in X component <u>m</u> in X component <u>m</u> in X component <u>m</u> in X component <u>m</u> in DxD/2 cells → Work item for each event n in N events add nth contribution to c of m → Seq.	for each component m in N component for each component m in N component for each component m in N/B events add mh contribution to cot m for each component m in N compo

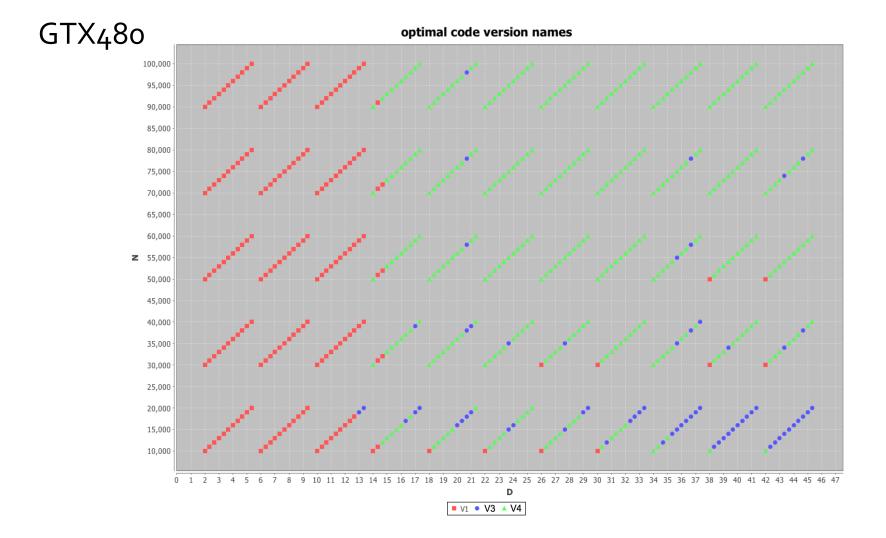




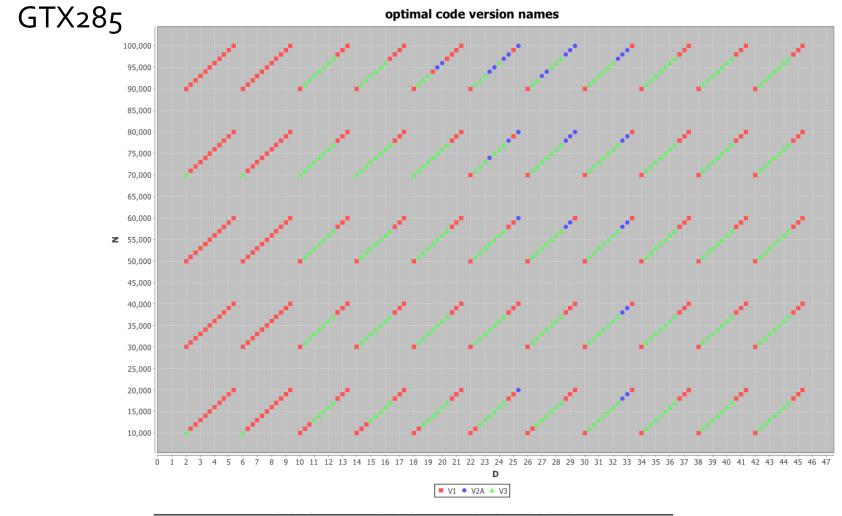
GMM Specializer: Overview



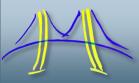
Results – Code Variant Performance



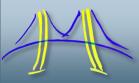
Results – Code Variant Performance



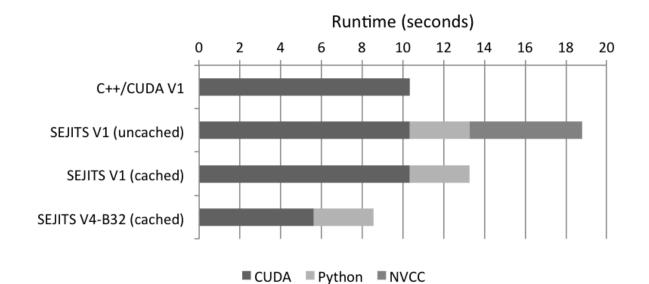
GPU	SMs	SIMD	Sh_mem Size	DRAM Size
GTX480	14	32	48KB	3GB
GTX285	30	8	16KB	1GB



- 32% average improvement in covariance matrix computation time using best code variant
 - compared to always using original hand-coded variant
 - D: 1 to 36, M: 1 to 128, N: 10K to 150K
- Performance gap increases with larger problem sizes
 - 75.6% for D=36, M=128, N=500,000

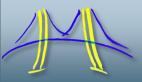


- Initial invocation 81% overhead due to complier invocations
- Future runs using automatically determined optimal code variant achieve 17% performance *improvement* over the original GPU implementation (V1)





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Speaker Diarization

Audio track:

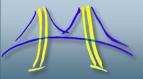
Segmentation:

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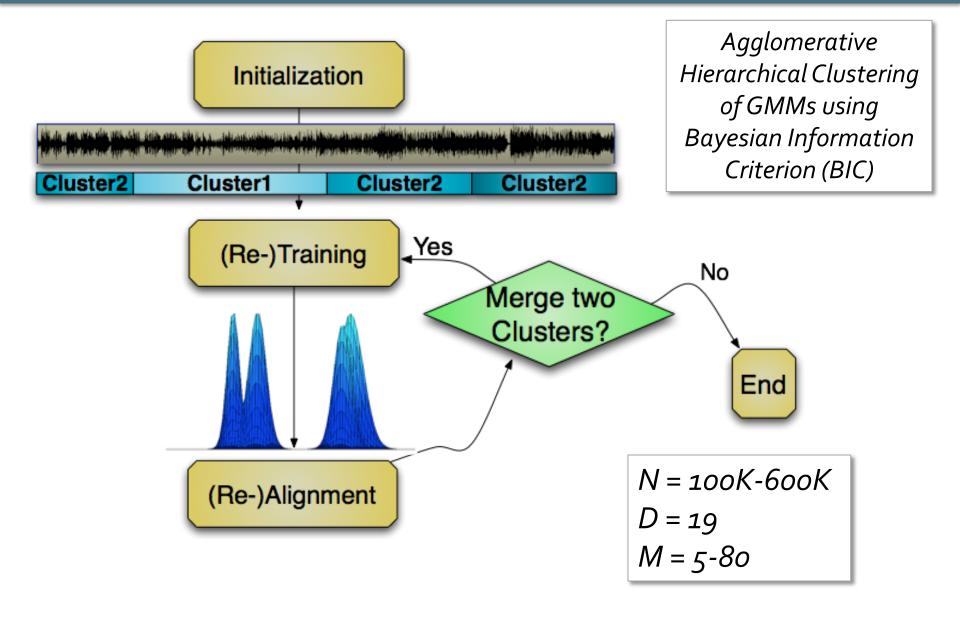
Clustering:

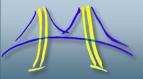
Speaker A	Speaker B	Speaker C	Sp. A		Speaker B
his da ana aka aka aka aka akar, kata wana tah ta ta .	and south and the state of the	histore china a cicle t ^{ala} tika in Alana baik	nellihissastemas <mark>s</mark>	ſ	Real part liver intervention and
vial (11) and a strain fragment field in the state of the P	Januari ang kasarang	landerska kalender for der beregen beskaldere	ke einde niensistien tie in		n ang alain a laind al it aid in dhe yn araith

Estimate "who spoke when" with no prior knowledge of speakers, #of speakers, words, or language spoken.

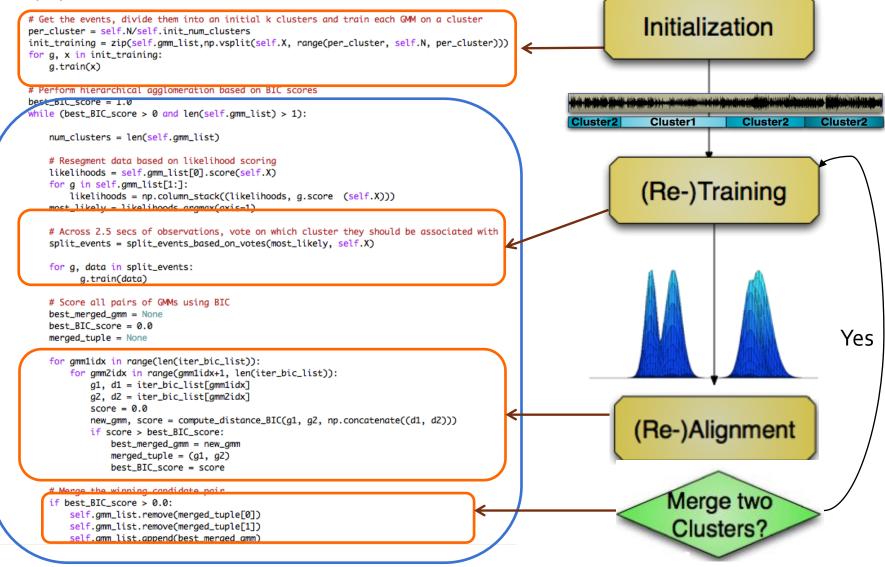


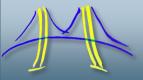
Speaker Diarization: Core Algorithm

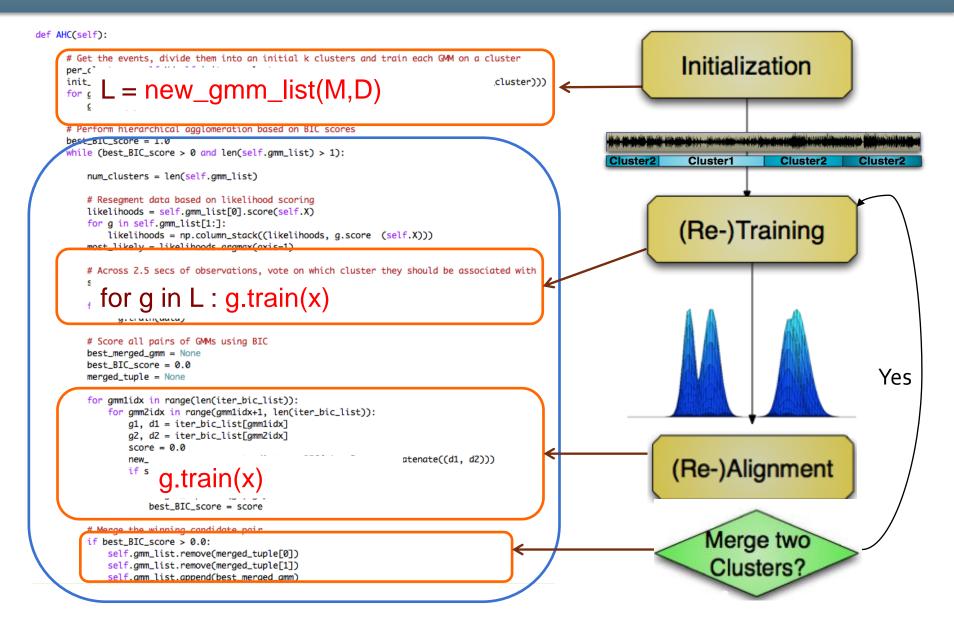


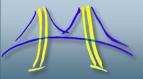


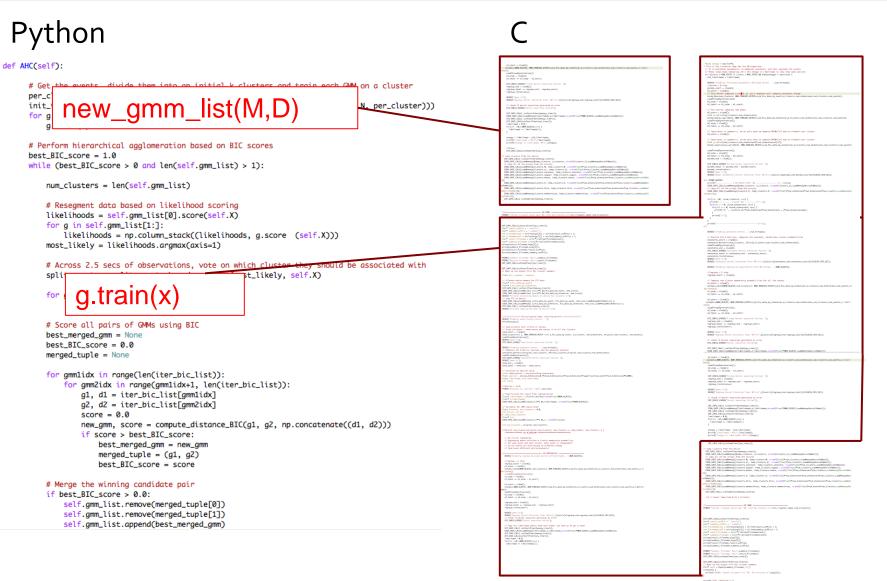
def AHC(self):

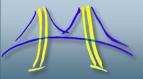












Python

def AHC(self):

Get the events, divide them into an initial k clusters and train each GMM on a cluster per_cluster = self.N/self.init_num_clusters init_training = zip(self.gmm_list,np.vsplit(self.X, range(per_cluster, self.N, per_cluster))) for g, x in init_training: g.train(x) # Perform hierarchical agglomeration based on BIC scores best BIC score = 1.0 while (best_BIC_score > 0 and len(self.gmm_list) > 1): num_clusters = len(self.gmm_list) # Resegment data based on likelihood scoring likelihoods = self.gmm_list[0].score(self.X) for g in self.gmm_list[1:]: likelihoods = np.column_stack((likelihoods, g.sco) most_likely = likelihoods.argmax(axis=1) # Across 2.5 secs of observations, vote on which cluster split_events = split_events_based_on_votes(most_likely, s for a, data in split_events: g.train(data) # Score all pairs of GMMs using BIC best_merged_gmm = None best_BIC_score = 0.0 merged_tuple = None for gmm1idx in range(len(iter_bic_list)): for gmm2idx in range(gmm1idx+1, len(iter_bic_list)) g1, d1 = iter_bic_list[gmm1idx] g2, d2 = iter_bic_list[gmm2idx] score = 0.0new_gmm, score = compute_distance_BIC(g1, g2, np.concatenate() if score > best_BIC_score: best_merged_gmm = new_gmm merged_tuple = (g1, g2)best BIC score = score

Merge the winning candidate pair if best_BIC_score > 0.0: self.gmm_list.remove(merged_tuple[0]) self.gmm_list.remove(merged_tuple[1]) self.gmm_list.append(best_merged_gmm) 15x Linesof-code reduction

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// sheek if kernel execution generated an error DF_DEDLERRORCHernel execution feiled");

// If re-estimates parameters, re-computes constants, and then regroups the events // Trans steps keep repeating web3 the change is likelihood is less than some epsil whole(thers < MRLTHES II (then < MRLTHES II & description)) { edd.3(bit(thena = 10kel/topa) BEBBC/Inveking resultate_parameters (+ //person = N stap parame_tate = clock(); s1_start = clock(); // Dris kernel computes a real (, pi tan't mate_ficeom_clockers, NMC_TMEADS_MIDE); miter,mennicogridume, cudefhreadlynchrenize(); m2_step = sime(); m2_tete1 -= m2_step - m2_stert; // Devariance is symmetric, so we only need to comp #3_start = clack(); // Environments is symmetric, so we only need to compute MP(N-1)/2 and gridDist(yww.sineters.yww.simemiares(yww.simemiares()/2)) mates.coverience.org/10/10/, NML/NEEDS.SUDDoo(s.fr.dos.bu. PRENE MATRIES 00000Clineking regroup //regree = E step regreep.stort = clock(); // Compute new cluster membership probabilities for all the cl_start = clack(); entrelecedim(OAR_LGOOS, non_clusters); NAN_THERAS_LESTIPses (itent);
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sl_step1 += sl_step - sl_stert; e2.stort - cleck(); steal-o-NN ROCKS, NN THEAD, C); culathreadlynchranize(); e2_stap = e1aeb(); e2_teta1 ← e2_stop - e2_stort; MEMBE("done.\n"): GEBBE("Reproduction Time: Mfutha",((d // check if kernel execution generated on error CMT_OMECK_ENNING_Terrol execution fuller[]: s2_utars = clack(); synsploceMM_R.COS, MM_THEAD, ISTO2000 OT,OED,DRBC'L-stop Kinnel execution fails regress,and = clash() regress,and = regress,and = regress,atart; regress iterations... MEMOCYCLER (MY); MEMOCYCLER Kernel Dierwiise Dies: MYs/s/, DIT_MAT_CALL(subfaritiesr(mempy_timer)) DIDE_SATE_CALL(subdMerryy()tkrithods.d.1tk DIT_SATE_CALL(subfaritmer(mempy_timer)); DIT_MAT_CALL(subfaritimer(spu_timer)); DIT_MAT_CALL(subfaritimer(spu_timer)); DIST_MAT_CALL(subfaritimer(spu_timer)); change = likeliheed - eld_likeliheed; prist(fileLiheed - NFWT_likeliheed); scient(fileLiheed - NFWT_likeliheed); (terms) NF WF (ALL(outStepTimer(coultimer)) (3)) OEM_SME_CALL(rudeMenopyCrimiters.B, temp. Editation); OEM_SME_CALL(rudeMenopyCrimiters.Bits, te yNet(refettor)); OEM_SME_CALL(rudeMenopyCrimiters.membersh);

> CHT_SWE_CALL(cutSportTimer(cpu_timer)); ther result_saffix = "results";

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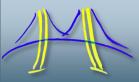
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.....

BERRY Mark VC'N BERRY Mark North Distantian Disc Monthly 27 shok of knowl associate generated as some OK, DBCA, DBBC / Second association follow?

// Easy the likelihood totals from each block, sum them up to get a total $\Omega \pi_{\rm c} {\rm MPL}_{\rm c} {\rm ALL}_{\rm c}$ cutioarthour(memory,like());

s2_start = clash(); estep2coMM_RLCCC, NON_THEADS

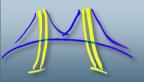


Diarization Error Rate (DER) and faster-than-real-time factor for the AMI Meeting Corpus

Average 71-115X Faster Than Real-Time Performance on NVIDIA Fermi GPU

FF DER	FF ×RT	NF DER	NF ×RT
40.99%	$71.19 \times$	25.38%	$72.83 \times$
27.38%	80.88 imes	32.34%	$163.22 \times$
41.28%	70.02 imes	10.57%	$123.28 \times$
46.83%	59.71 imes	28.40%	$177.80 \times$
41.54%	80.85 imes	34.30%	$\boldsymbol{254.81\times}$
66.89%	64.33 imes	50.75%	56.13 imes
29.88%	74.03 imes	16.57%	129.35 imes
63.68%	54.87 imes	$\mathbf{53.05\%}$	58.36 imes
2.19%	64.29 imes	$\mathbf{1.65\%}$	60.35 imes
4.99%	81.46 imes	8.58%	$151.80 \times$
32.43%	67.20 imes	9.30%	$81.13 \times$
27.84%	83.42 imes	26.27%	55.77 imes
35.49%	$71.02 \times$	24.76%	$115.40 \times$
	$\begin{array}{c} 40.99\%\\ 27.38\%\\ 41.28\%\\ 46.83\%\\ 41.54\%\\ \textbf{66.89\%}\\ 29.88\%\\ 63.68\%\\ \textbf{2.19\%}\\ 4.99\%\\ 32.43\%\\ 27.84\%\end{array}$	$\begin{array}{cccc} 40.99\% & 71.19\times \\ 27.38\% & 80.88\times \\ 41.28\% & 70.02\times \\ 46.83\% & {\bf 59.71}\times \\ 41.54\% & 80.85\times \\ {\bf 66.89\%} & 64.33\times \\ 29.88\% & 74.03\times \\ 63.68\% & 54.87\times \\ {\bf 2.19\%} & 64.29\times \\ 4.99\% & 81.46\times \\ 32.43\% & 67.20\times \\ 27.84\% & {\bf 83.42\times } \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

[6] Ekaterina Gonina, Gerald Friedland, Henry Cook, Kurt Keutzer "Fast Speaker Diarization Using a High-Level Scripting Language" In Proceedings of IEEE Workshop on Automatic Speech Recognition and Understanding (ASRU), Dec 11-15, 2011, Waikoloa, Hawaii.



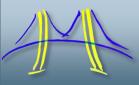
Mic Array	Py+Cilk+ Py+CUDA	
	Westmere	GTX285/GTX480
Near field	$56 \times$	$101 \times / 115 \times$
Far field	$32 \times$	$68 \times / 71 \times$

- Faster-than-real-time factors for:
 - Specializer on Intel Westmere (12 cores/24 threads)
 - Nvidia GTX280 & GTX480

GPU	SMs	SIMD	Sh_mem Size	DRAM Size
GTX480	14	32	48KB	3GB
GTX285	30	8	16KB	1GB



- 1. Parallelism & productivity-performance gap
- 2. Proposed solution: a Specialization Framework <
- Example: Gaussian mixture model (GMM) training specializer
- 4. Example applications using GMM specializer:
 - 1. Speaker diarization ┥
 - 2. Music recommendation system
- 5. Summary
- 6. Future work

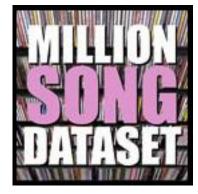


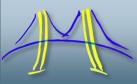
- Given a query song or subset of songs return similar songs
- Song recommendation system based on the *content* of the audio files
 - Audio segment-based features
- No need for tedious manual tagging!
- Can use any audio for querying
 - Your iTunes library?
 - Recording from a concert?
 - Humming your favorite song?





- Million Song Dataset (MSD) from Columbia University: <u>http://labrosa.ee.columbia.edu/millionsong/</u>
- "A freely-available collection of audio features and metadata for a million contemporary popular music tracks"
- IM song features & metadata
 - Artist & song information
 - Tags & beat information
 - MFCC-like timbre features



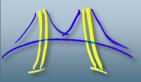


- Based on the UBM*-GMM supervector approach (IRCAM'10 [6]) (next slide)
- 1. Offline Phase: train UBM & song models
- 2. Online Phase: train query model & return top 10 closest songs

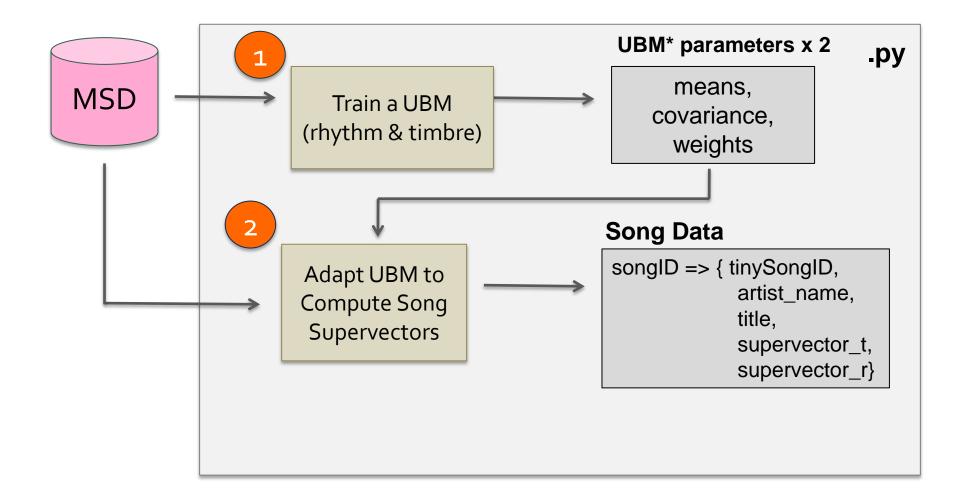


[6] C. Charbuillet, D. Tardieu, F. Cornu, and G. Peeters, "2011 IRCAM AUDIO MUSIC SIMILARITY SYSTEM#," 2011.

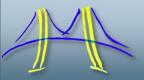
UBM* = Universal Background Model



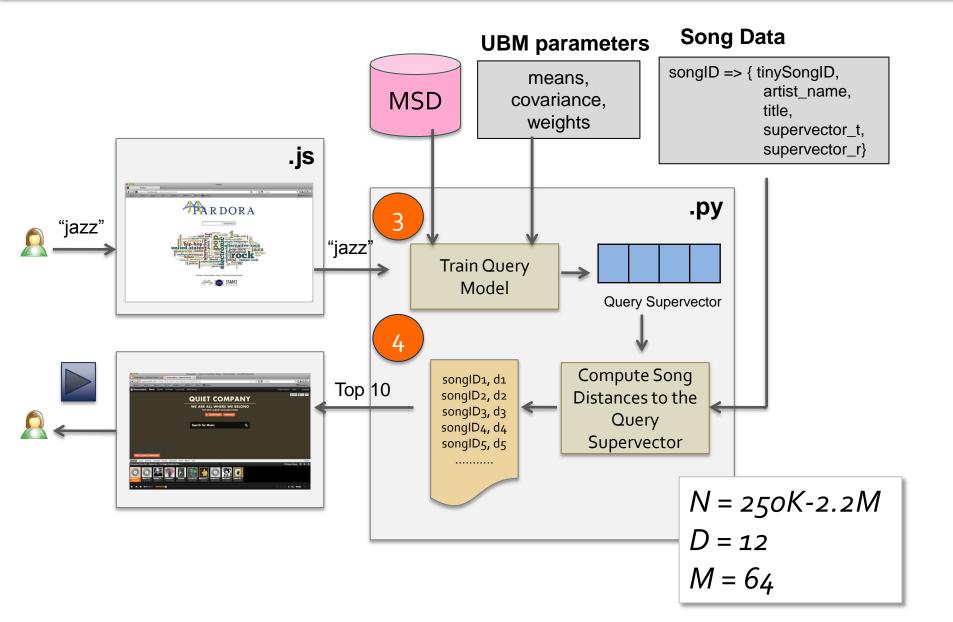
Pardora – Offline Phase

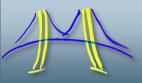


UBM* = Universal Background Model

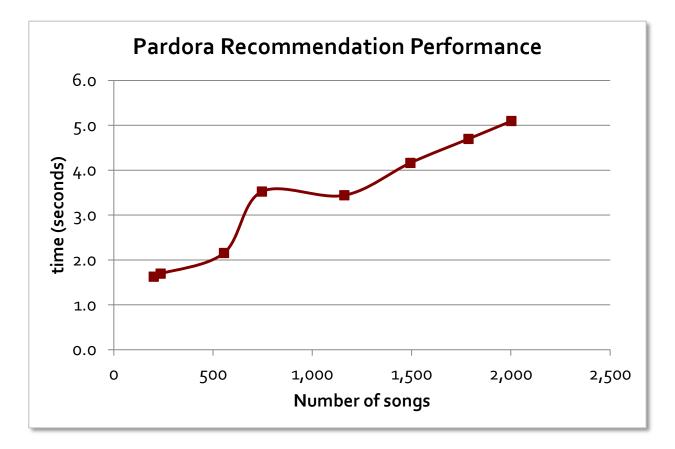


Pardora – Online Phase





- Offline Phase: ~10 minutes
- Online Phase: 1.5-5 seconds depending on query size





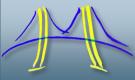
- 1. Parallelism & productivity-performance gap
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 - 1. Speaker diarization
 - 2. Music recommendation system 🗸
- 5. Summary
- 6. Future work



- Programming parallel processors is challenging
- Selective JIT specialization can allow us to bridge the productivity-performance gap
- Example: Gaussian Mixture Model specializer
 - Python-level productivity & CUDA-level performance
- Two example applications:
 - Speaker Diarization (~100 lines of Python)
 - 71-115x faster-than-real-time performance
 - Music Recommendation System (~600 lines of Python)
 - Order of seconds for online recommendation
 - Productivity meets performance

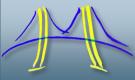


- Scalability of this approach for application development
 - More applications using more specializers
 - Focus on productivity & performance
- Scalability to the cloud for large datasets
 - Whole 1M songs will require cluster-level parallelism
- Autotuning and smarter code generation/selection
 - Incorporate machine learning & heuristics
- Specializer composition
 - Optimization & data structure selection

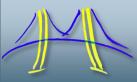


Thank you!

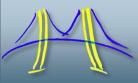
Research supported by Microsoft (Award #024263) and Intel (Award #024894) funding and by matching funding by U.C. Discovery (Award #DIG07-10227). Additional support comes from Par Lab affiliates National Instruments, Nokia, NVIDIA, Oracle, and Samsung.



Backup Slides

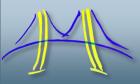


- User writes code for a structured grid calculation
- # 3d heat equation def kernel(inArray, outArray): for pt in inArray.interior(): for x in pt.neighbors(radius=1): outArray[pt] += 1/6 * inArray[x]



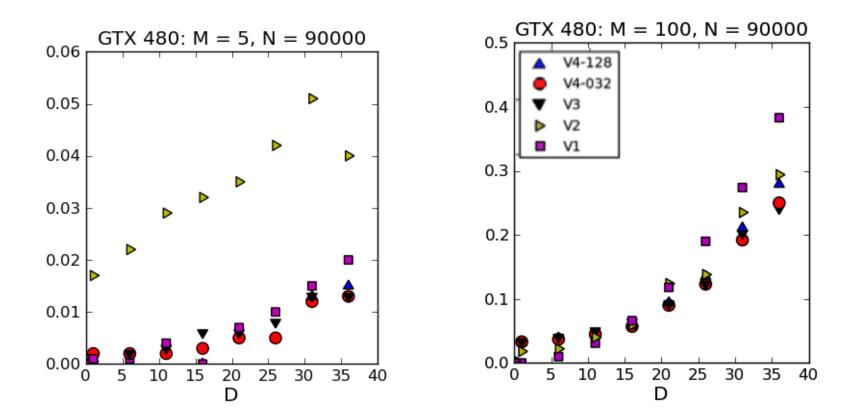
- When the user runs kernel(A,B):
 - Python code is transformed into optimized C code (more on that later)
 - Take into account # of cores, size of array (256³)

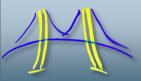
```
int c2;
for (c2=chunkOffset 2;c2<=255;c2+=128) {</pre>
 int c1;
 for (c1=chunkOffset 1;c1<=255;c1+=64) {</pre>
  int c0;
                                                                   grid
  for (c0=chunkOffset 0;c0<=255;c0+=256) {</pre>
   int b2;
                                                                                                            register
                                                                   core blocks
   for (b2=c2 + threadOffset 2;b2<=c2 + 127;b2+=128) {</pre>
                                                                                                            block
    int b1;
    for (b1=c1 + threadOffset 1;b1<=c1 + 31;b1+=16) {</pre>
     int b0;
                                                                                      thread blocks
     for (b0=c0 + threadOffset 0;b0<=c0 + 255;b0+=256) {</pre>
      int kk;
      for (kk=b2 + 1;kk < b2 + 128;kk+=1) {
       int jj;
       for (jj=b1 + 1;jj<=b1 + 16;jj+=1) {
         int ii;
         for (ii=b0 + 1;ii<=b0 + 256;ii+=1) {</pre>
          dst[ dst Index(ii - 1,jj - 1,kk - 1)] = ...;
                                                                                52
}}}}}
```



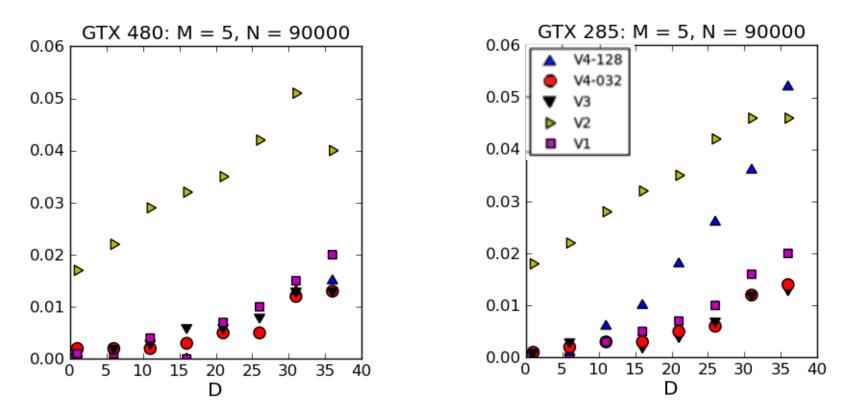
- When the user runs kernel(A,B):
 - Python code is transformed into optimized C code (more on that later)
 - Code is output to disk
 - Compiler runs, turns it into dynamic library
 - Library is loaded into the interpreter
 - Translated function is called & result returned to interpreter
- To user, it just looks like the code ran really fast

NVIDIA GTX480 – Varying D



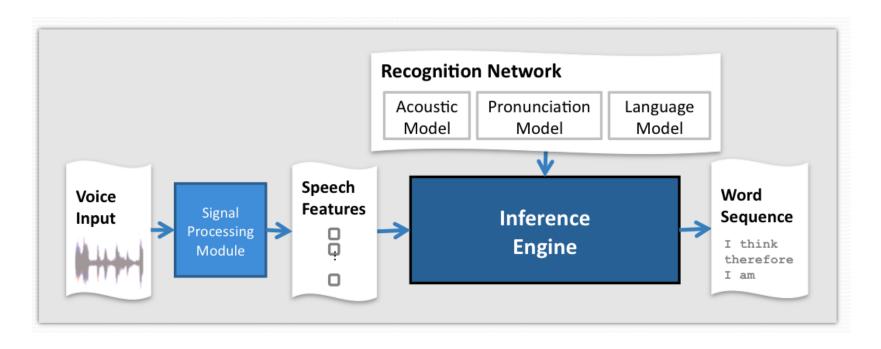


NVIDIA GTX285 vs. 480



GPU	SMs	SIMD	Sh_mem Size	DRAM Size
GTX480	14	32	48KB	3GB
GTX285	30	8	16KB	1GB

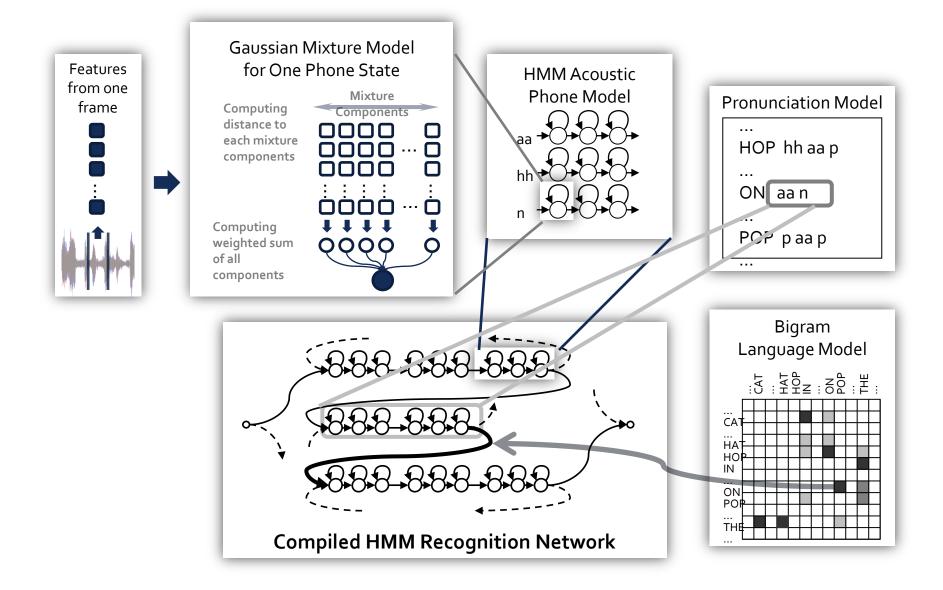
Example: Speech Recognition



- Task: recognize words and sentences from an audio file
 - Recognizing words from a large vocabulary arranged in exponentially many possible permutations
 - Inferring word boundaries from the context of neighboring words
- Viterbi decoding on Hidden Markov Models

Jike Chong, Ekaterina Gonina, Kurt Keutzer, "Efficient Automatic Speech Recognition on the GPU" Chapter in GPU Computing Gems Emerald Edition, Morgan Kaufmann, Vol. 1, February 9, 2011.

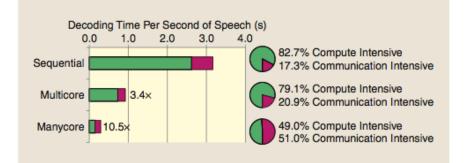
Example: Speech Recognition

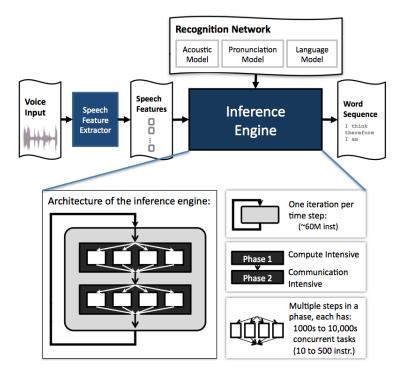


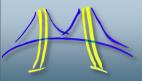
My Previous Work (1)

Fully-parallel Speech Recognition Decoder

- Efficient multicore and manycore implementations of entire decoder (InterSpeech'09)
- Exploring
 - Algorithmic-level design space (IEEE SP Journal 2009)
 - Recognition network representation (InterSpeech'11)

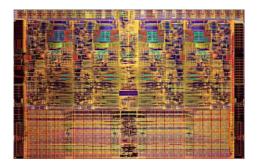




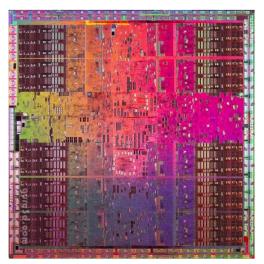


Multicore & Manycore, cont.

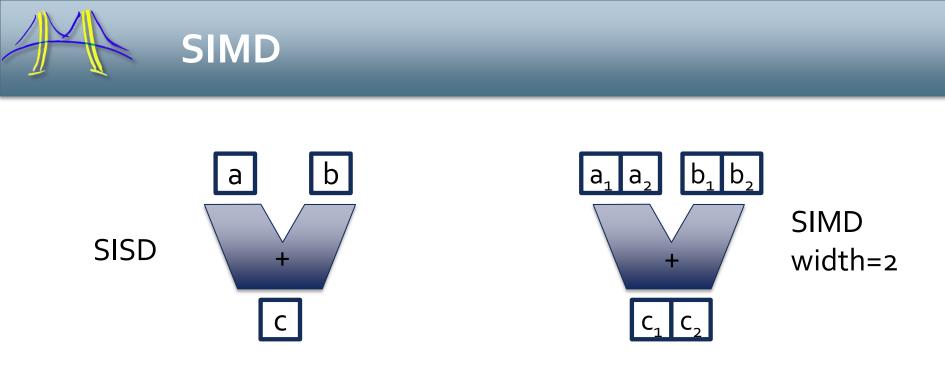
Specifications	Core i7 960	GTX285
Processing Elements	4 cores, 4 way SIMD @3.2 GHz	30 cores, 8 way SIMD @1.5 GHz
Resident Strands/Threads (max)	4 cores, 2 threads, 4 way SIMD: 32 strands	30 cores, 32 SIMD vectors, 32 way SIMD: 30720 threads
SP GFLOP/s	102	1080
Memory Bandwidth	25.6 GB/s	159 GB/s
Register File	-	1.875 MB
Local Store	-	480 kB



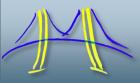
Core i7 (45nm)



GTX285 (55nm)

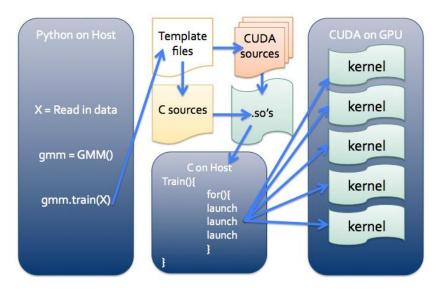


- Single Instruction Multiple Data architectures make use of data parallelism
- We care about SIMD because of area and power efficiency concerns
 - Amortize control overhead over SIMD width
- Parallelism exposed to programmer & compiler



GMM Specializer: Details

- Python application code
 - Manipulates problem data, sets up application logic
- C/CUDA code that runs quickly
 - Allocates GPU memory
 - Performs main EM iterative loop
- Specializer [5]
 - Selects appropriate code variant (from history) based on parameters
 - Pulls in the template for the code variant, parameterizes it and compiles to binary



[5] H. Cook, E. Gonina, S. Kamil, G. Friedland, D. Patterson, A. Fox. "CUDA-level Performance with Python-level Productivity for Gaussian Mixture Model Applications" In Proceedings of the 3rd USENIX conference on Hot topics in parallelism (HotPar'11). USENIX Association, Berkeley, CA, USA.